

<b>Notice of References Cited</b>	Application/Control No. 09/740,411	Applicant(s)/Patent Under Reexamination CHEN ET AL.	
	Examiner Aravind K Moorthy	Art Unit 2131	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,085,210	07-2000	Buer, Mark Leonard	708/491
	B	US-6,055,619	04-2000	North et al.	712/36
	C	US-6,047,372	04-2000	Thayer et al.	712/222
	D	US-6,009,505	12-1999	Thayer et al.	712/6
	E	US-6,006,245	12-1999	Thayer, John S.	708/404
	F	US-5,941,938	08-1999	Thayer, John S.	708/490
	G	US-5,931,892	08-1999	Thome et al.	708/322
	H	US-5,909,572	06-1999	Thayer et al.	712/226
	I	US-5,893,145	04-1999	Thayer et al.	711/125
	J	US-5,801,975	09-1998	Thayer et al.	708/402
	K	US-5,680,600	10-1997	Childers et al.	712/245
	L	US-5,452,425	09-1995	Childers et al.	712/241
	M	US-4,213,188	07-1980	Smolko et al.	708/532

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP0504996A2	09-1992	EP	Kessels, Jozef Laurentius	G06F 7/72
	O	EP0449349A1	10-1991	EP	Unknown	G09C 1/00
	P	EP0281303A2	09-1988	EP	Omura, Jimmy	G06F 7/60
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Prasanna et al, Fast VLSI Architectures Using Non-Redundant Multibit Recoding for Computing Exponentiation Modulo A Postitive Integer, 1991, IEEE, pages 3054-3057
	V	Kornerup, Peter, High-Radix Modular Multiplication for Cryptosystems, 1993, IEEE, pages 277-283
	W	Ma et al, Publicly Verifiable Authenticated Encryption, 2003, IEEE, pages 281-282
	X	Pereira, Roy, A Primer on High-level Wireless Security, 2004, Wireless Design & Development, pages 28, 30 and 32

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.